



## TM03N10BI

## N-Channel Enhancement Mosfet

## General Description

- Low  $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

## Applications

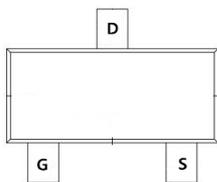
- Load switch
- PWM

## General Features

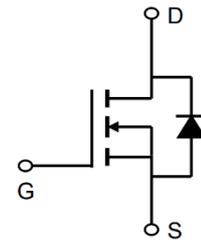
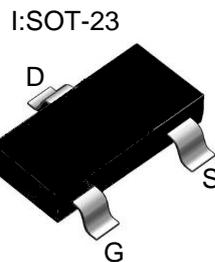
$V_{DS} = 100V \quad I_D = 2.8A$

$R_{DS(ON)} = 220m\Omega(\text{typ.}) @ V_{GS} = 10V$

100% UIS Tested

100%  $R_g$  Tested

Marking: 1002 OR 3N10

Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Max.	Units	
$V_{DSS}$	Drain-Source Voltage	100	V	
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V	
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	2.8	A
		$T_A = 100^\circ\text{C}$	1.6	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	8.5	A	
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	2.3	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	54	$^\circ\text{C}/\text{W}$	
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$	



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**Electrical Characteristics** ( $T_J=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=100V, V_{GS}=0V$	-	-	1.0	$\mu A$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.2	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	$V_{GS}=10V, I_D=2A$	-	220	250	m $\Omega$
		$V_{GS}=4.5V, I_D=1A$	-	255	280	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	321	-	pF
$C_{oss}$	Output Capacitance		-	21	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	15	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=30V, I_D=2A,$ $V_{GS}=10V$	-	5.3	-	nC
$Q_{gs}$	Gate-Source Charge		-	1.3	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.7	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=30V,$ $I_D=1A, R_{GEN}=3\Omega,$ $V_{GS}=10V$	-	14	-	ns
$t_r$	Turn-on Rise Time		-	54	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	18	-	ns
$t_f$	Turn-off Fall Time		-	11	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	3.0	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	8.8	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=2.2A$	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 0.5\%$



### Typical Performance Characteristics

Figure 1: Output Characteristics

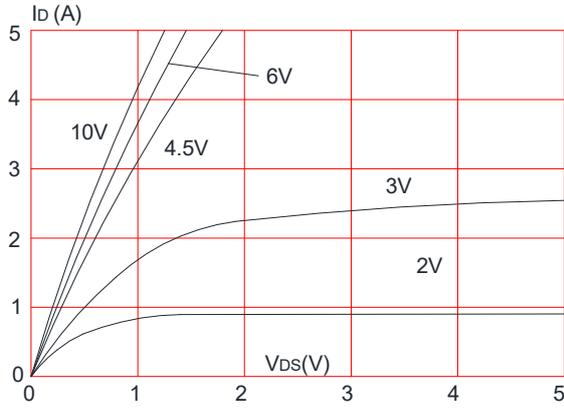


Figure 2: Typical Transfer Characteristics

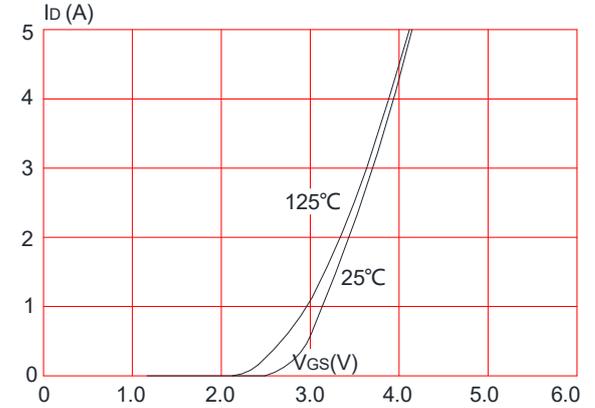


Figure 3: On-resistance vs. Drain Current

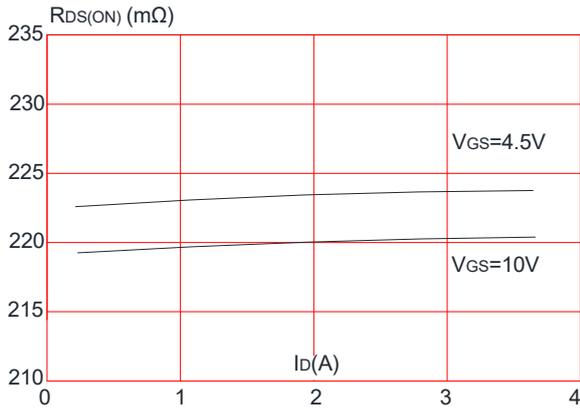


Figure 4: Body Diode Characteristics

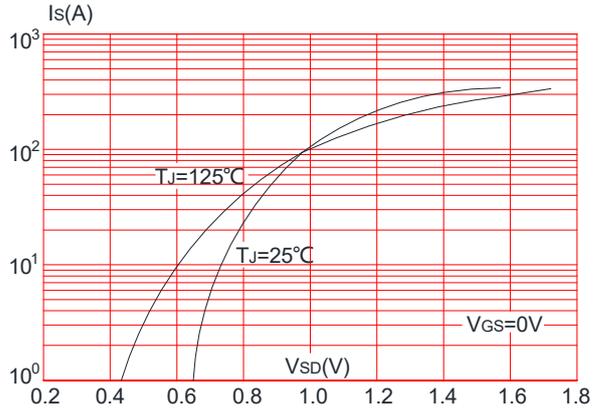


Figure 5: Gate Charge Characteristics

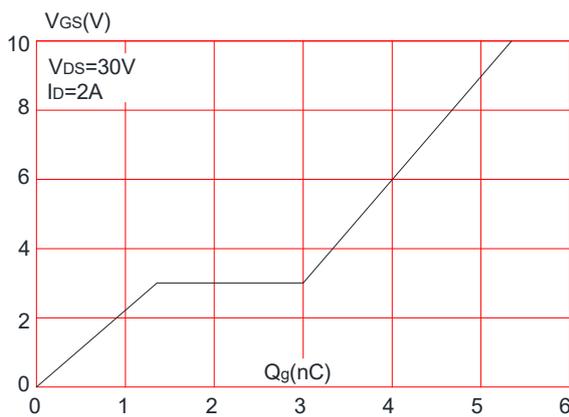
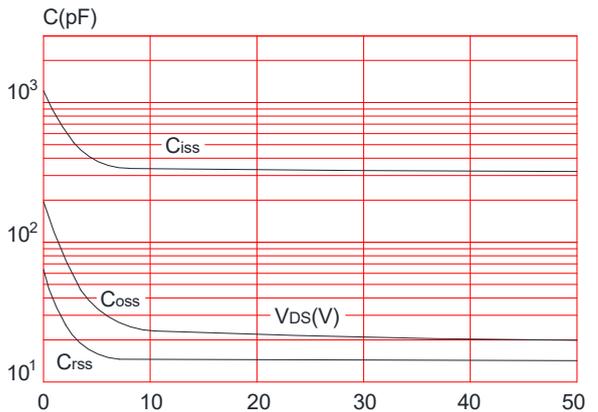


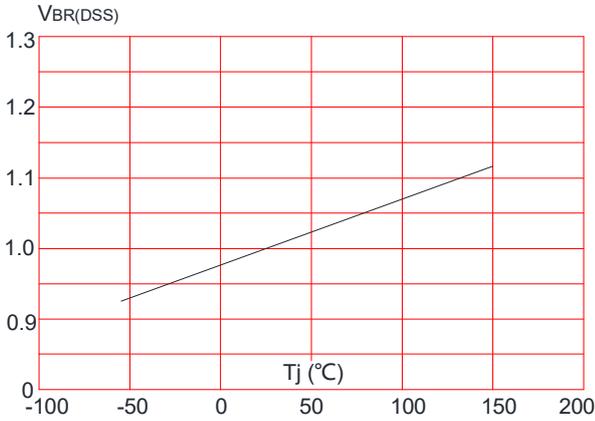
Figure 6: Capacitance Characteristics



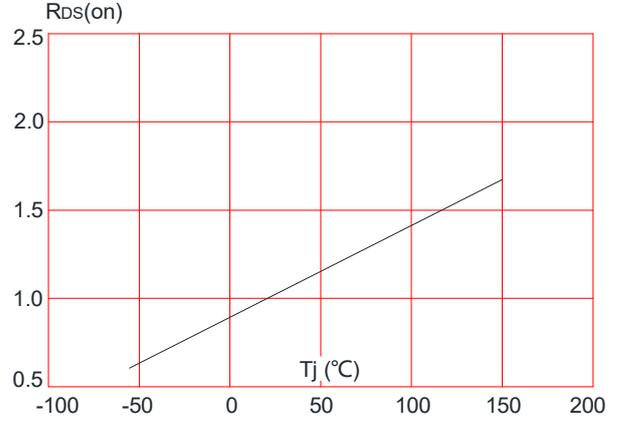
**TM03N10BI**

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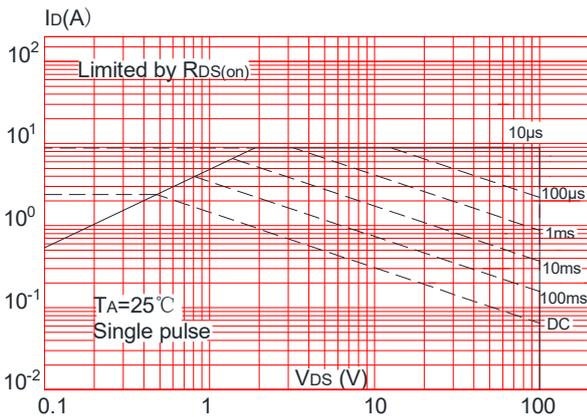
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



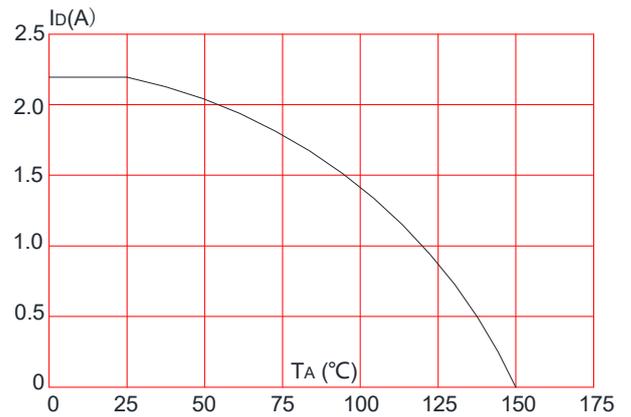
**Figure 8:** Normalized on Resistance vs. Junction Temperature



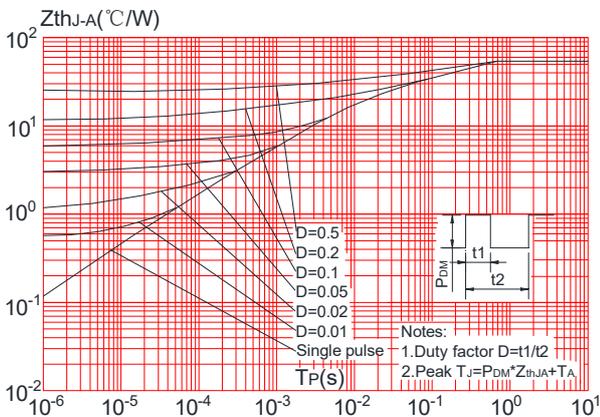
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



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Test Circuit

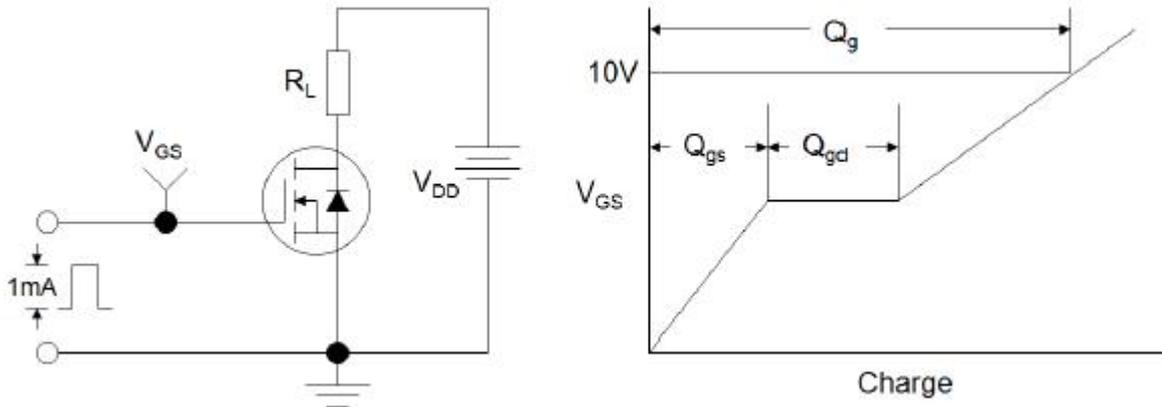


Figure1:Gate Charge Test Circuit & Waveform

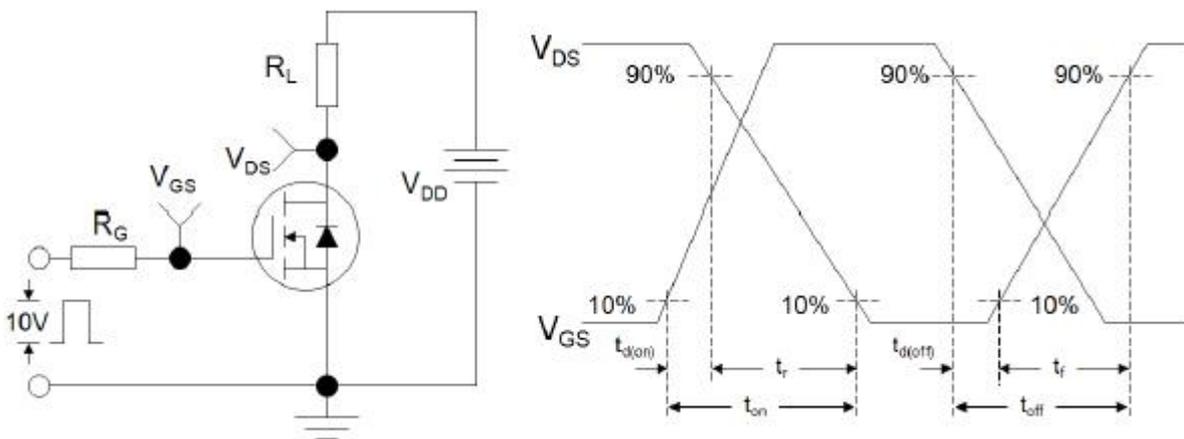


Figure 2: Resistive Switching Test Circuit & Waveforms

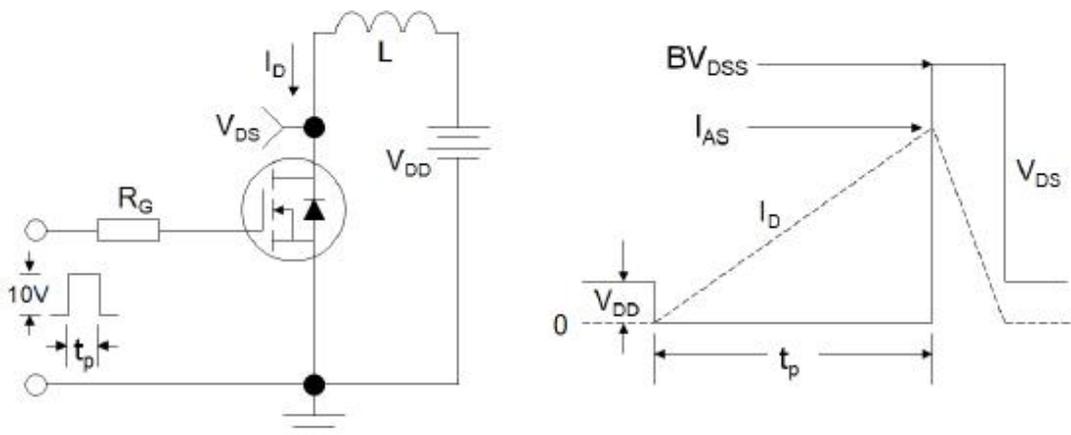
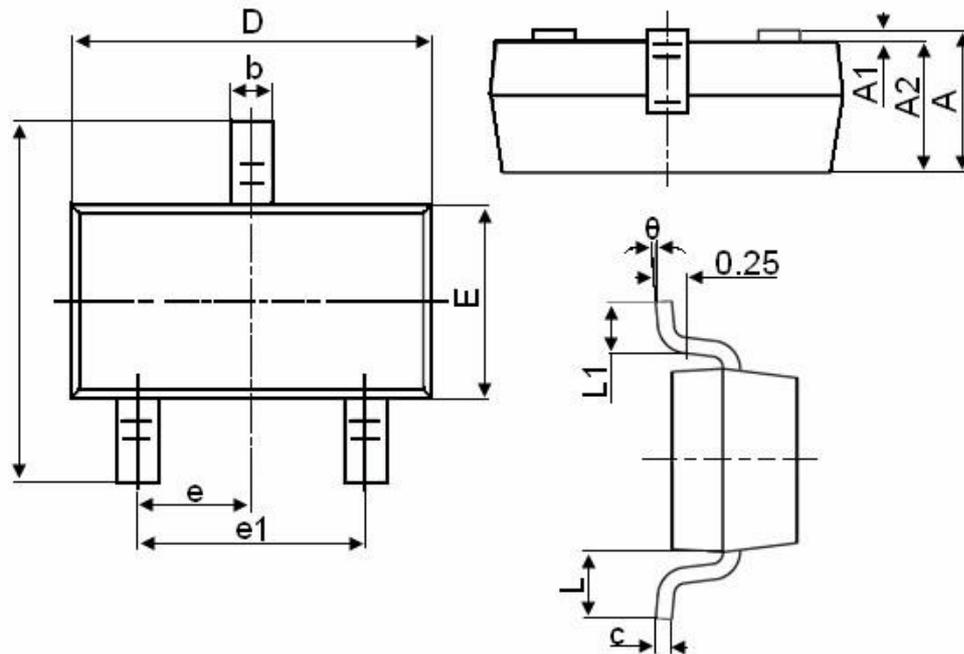


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Package Mechanical Data: SOT-23



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
$\theta$	0°	8°